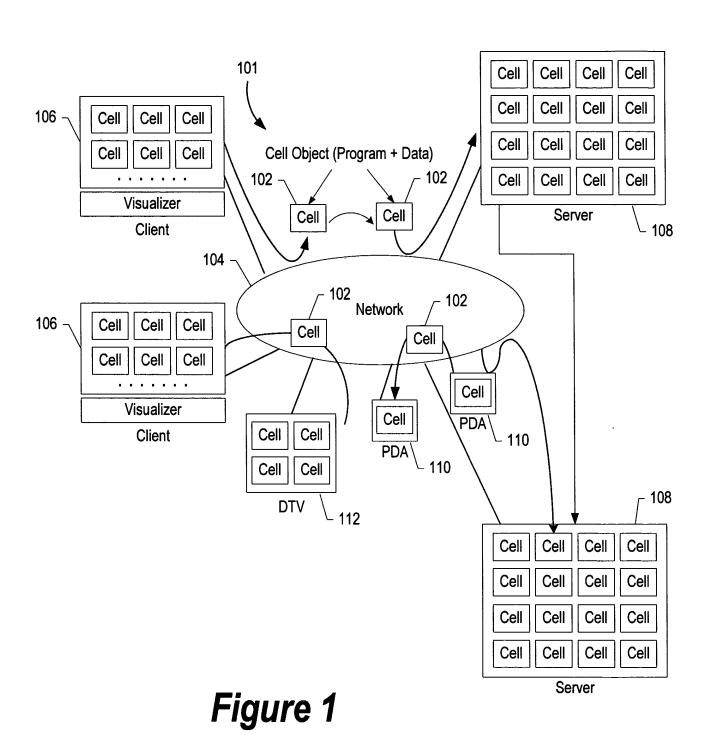
D ck tN . AUS920030702US1 Br kenshire, et al.

System and Meth df r Dynamically Partiti ning Proc ssing Acr ss Plurality fH terogene us Process rs



D cket N . AUS920030702US1 Br kenshire, tal.

System and Method for Dynamically Partiti ning Processing Acr ss Plurality of Heter gen us Pr cess rs

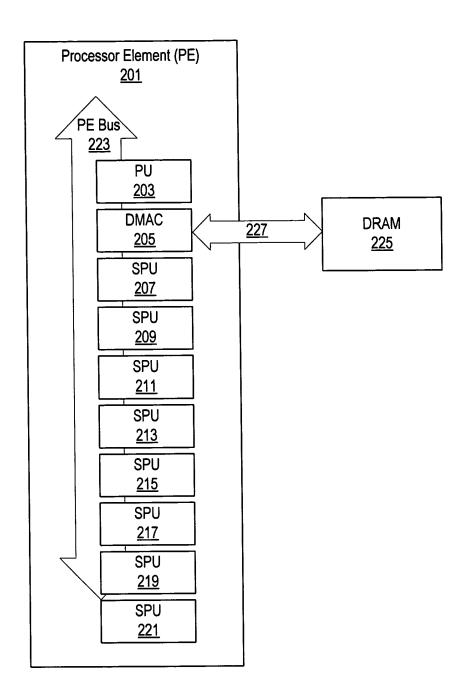


Figure 2

Docket No. AUS920030702US1 Br kenshire, t al.

System and Method f r Dynamically Partiti ning Pr cessing Acr ss Plurality of H terog neous Pr c ss rs

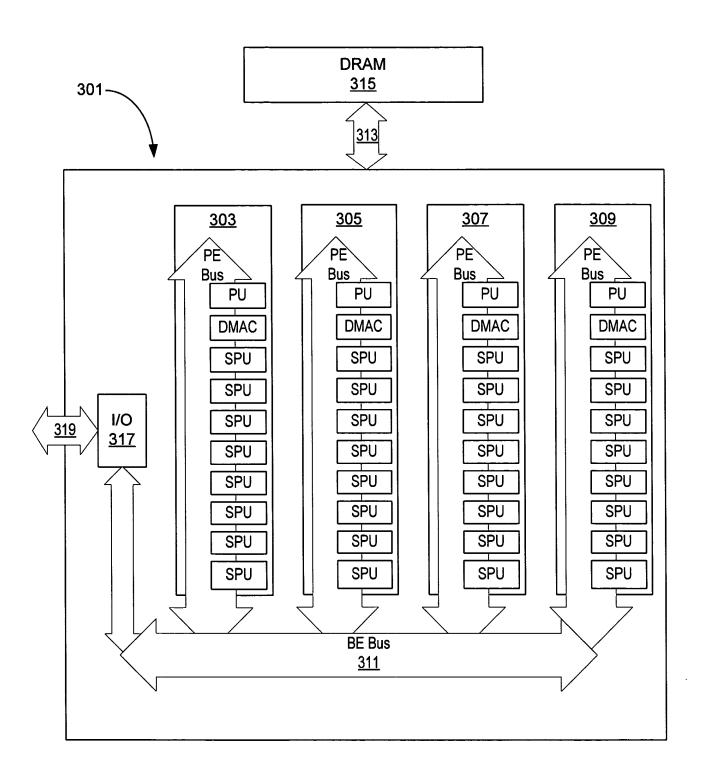


Figure 3

D ck t No. AUS920030702US1 Brokenshire, et al.

System and Meth d for Dynamically Partiti ning Pr c ssing Acr ss Plurality f H ter gen us Pr cess rs

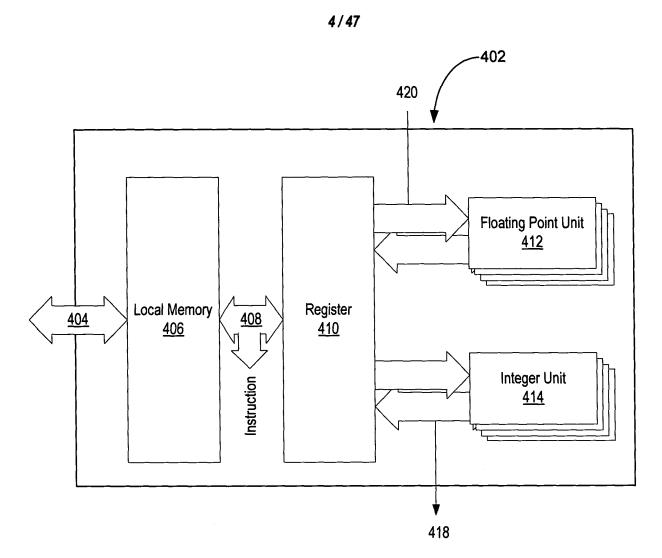


Figure 4

D cket N . AUS920030702US1 Brokenshire, et al.

System and Method for Dynamically Partitioning Pr c ssing Acr ss Plurality of Heterog n ous Pr cessors

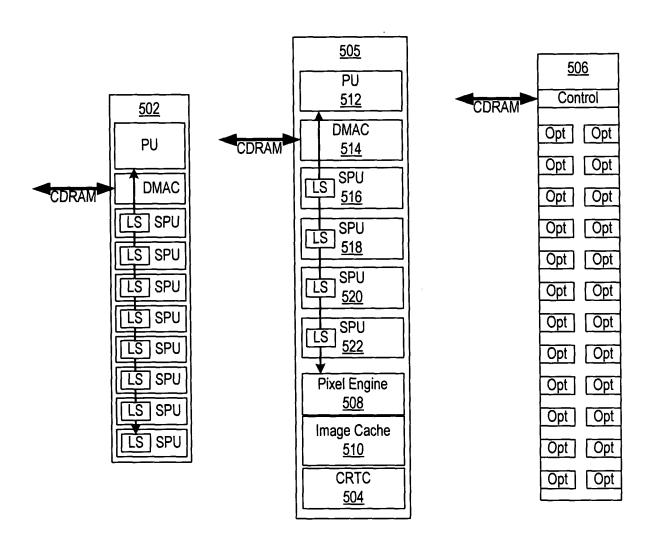
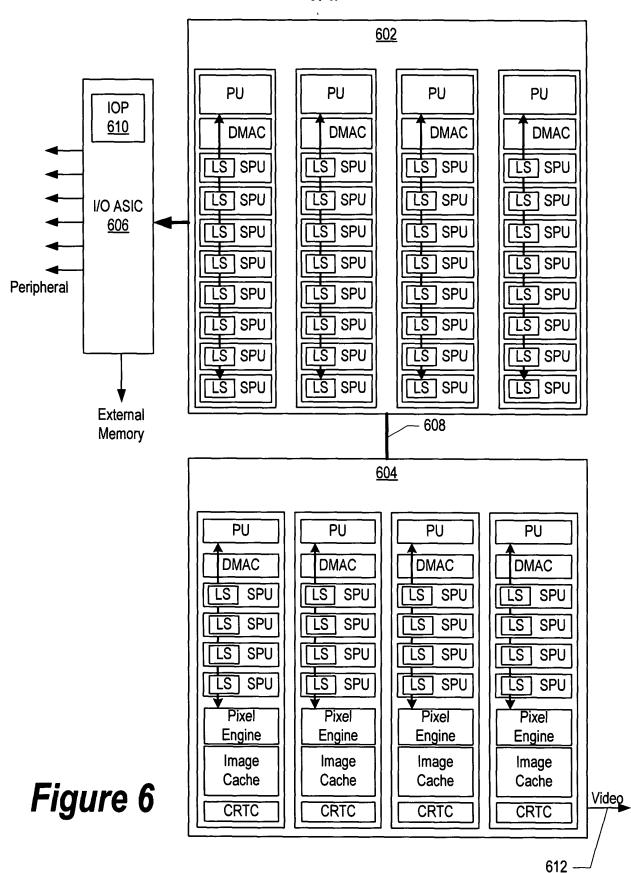


Figure 5

D cket N . AUS920030702US1 Brokenshire, t al.

System and Method for Dynamically Partitioning Proc ssing Across Plurality f H terog neous Proc ssors



D cket No. AUS920030702US1 Brokenshire, et al.

System and Method f r Dynamically Partitioning Pr cessing Across Plurality of H terog n ous Processors

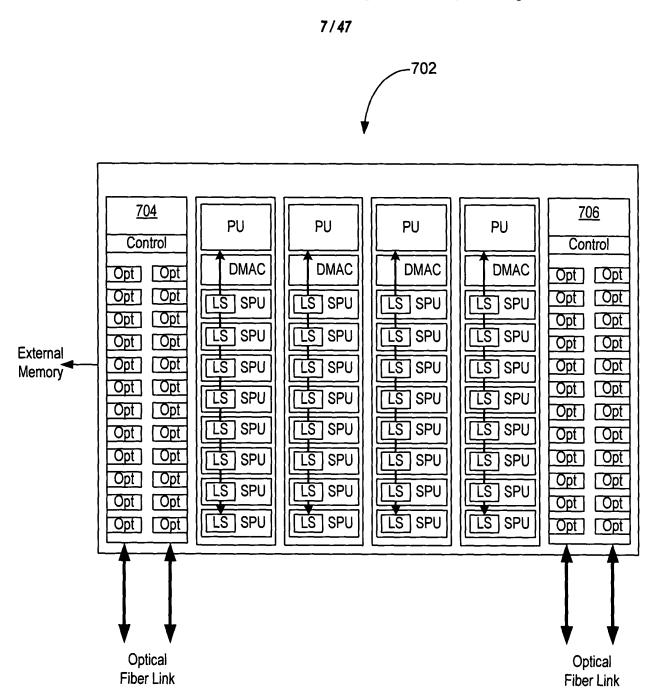


Figure 7

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D ck t N . AUS920030702US1 Br kenshire, et al.

System and M th d for Dynamically Partiti ning Processing Across Plurality f H terog neous Process rs

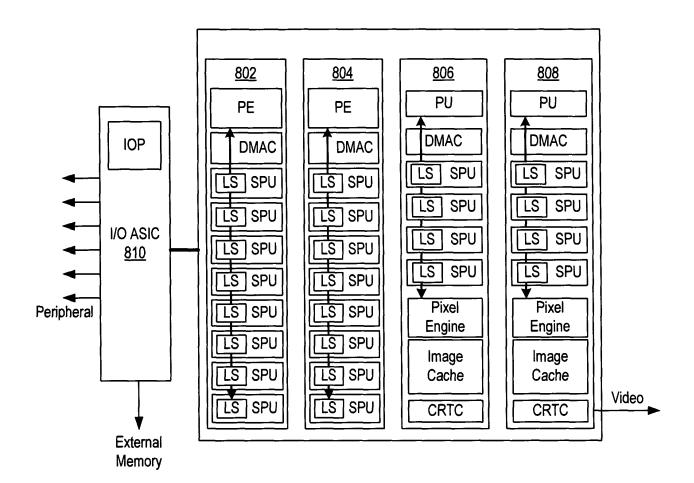


Figure 8

System and Method f r Dynamically Partiti ning Pr c ssing Across Plurality of H terog neous Proc ss rs

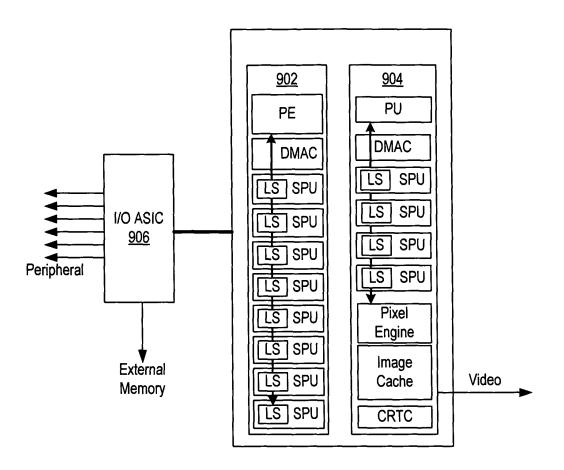


Figure 9

D cket N . AUS920030702US1 Br kenshire, tal.

System and M th d for Dynamically Partiti ning Processing Across Plurality of Heterogen us Pr c ssors

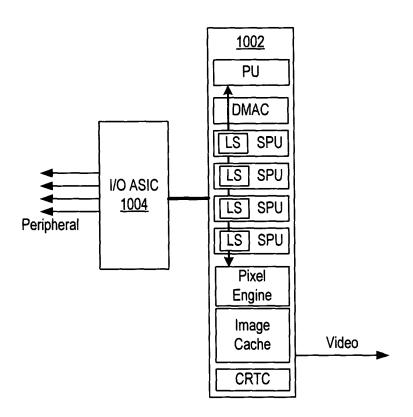


Figure 10

D cket N . AUS920030702US1 Br kenshire, et al.

System and Method for Dynamically Partitioning Pr c ssing Across Plurality of Heterogen ous Pr c ss rs

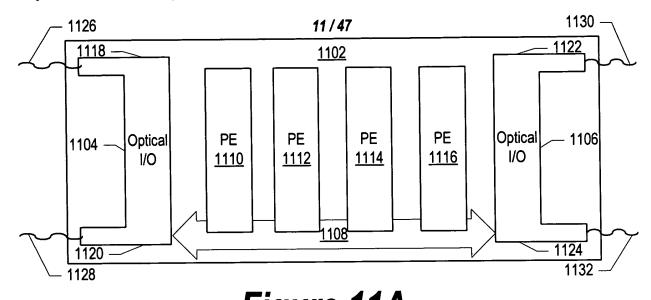


Figure 11A

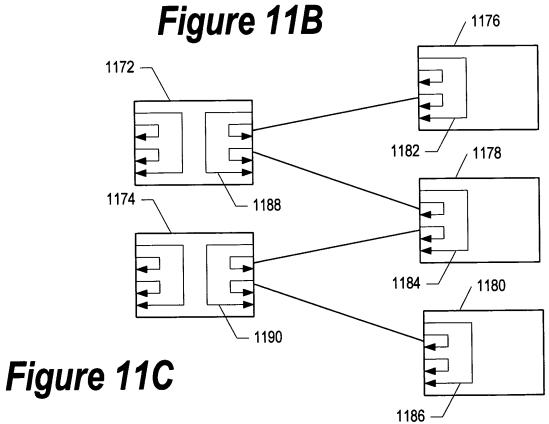
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1160

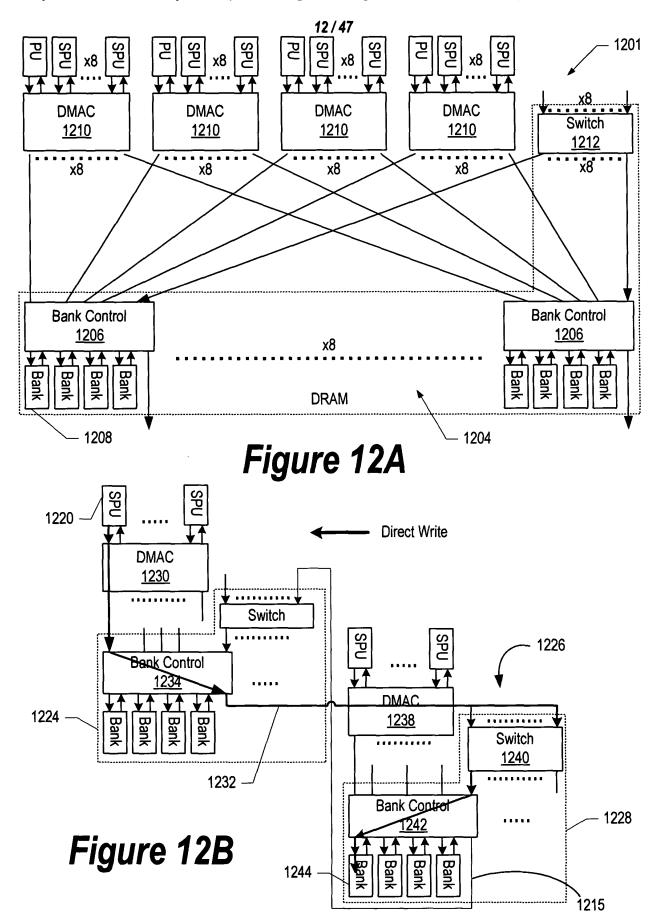
1162

1164



D ck tN . AUS920030702US1 Brok nshire, tal.

System and Meth df r Dynamically Partiti ning Proc ssing Acr ss Plurality f Heter gen us Pr c ss rs



D cket No. AUS920030702US1 Br kenshire, et al.

System and Meth d for Dynamically Partitioning Pr cessing Acr ss Plurality of H ter g n us Pr c ss rs

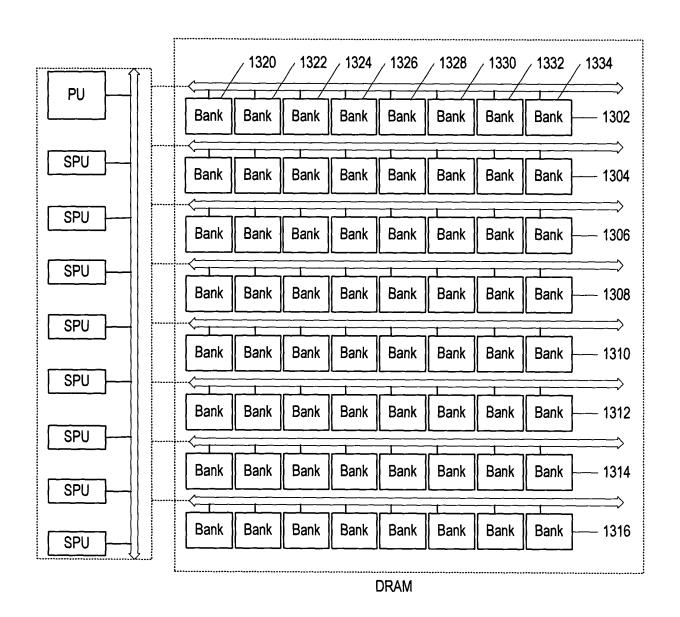


Figure 13

D cket N . AUS920030702US1 Br kenshire, tal.

System and Method for Dynamically Partiti ning Pr cessing Acr ss Plurality f Heterogeneous Processors

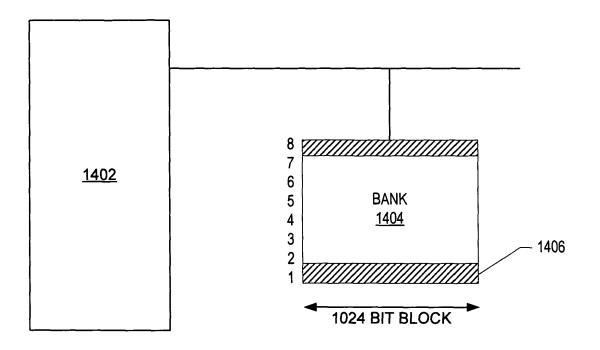


Figure 14A

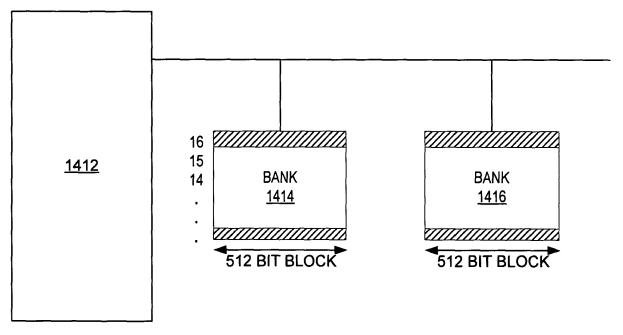


Figure 14B

System and Method f r Dynamically Partiti ning Pr cessing Across Plurality of H terog n us Pr c ss rs

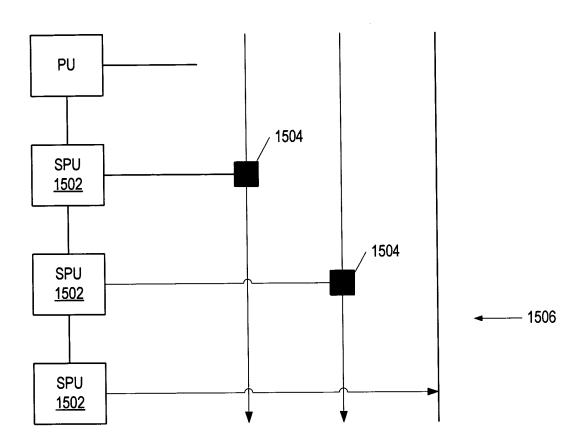


Figure 15

D ck t N . AUS920030702US1 Br kenshire, et al.

System and M th d for Dynamically Partitioning Pr cessing Acr ss Plurality of Heter gene us Pr cessors

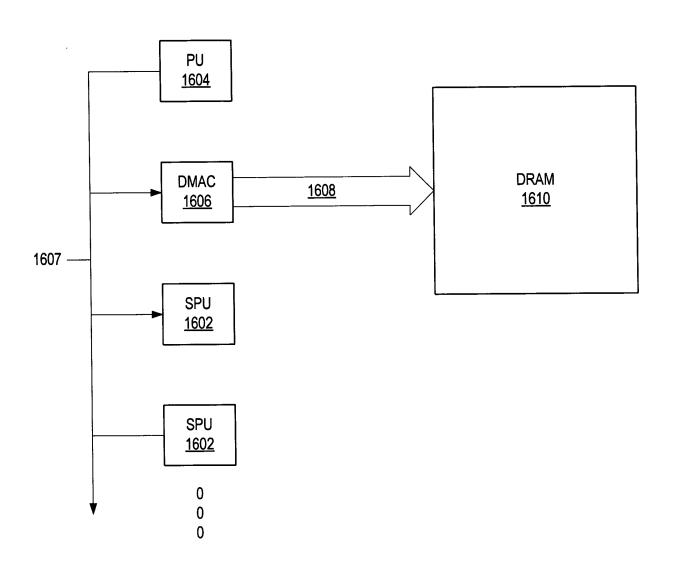
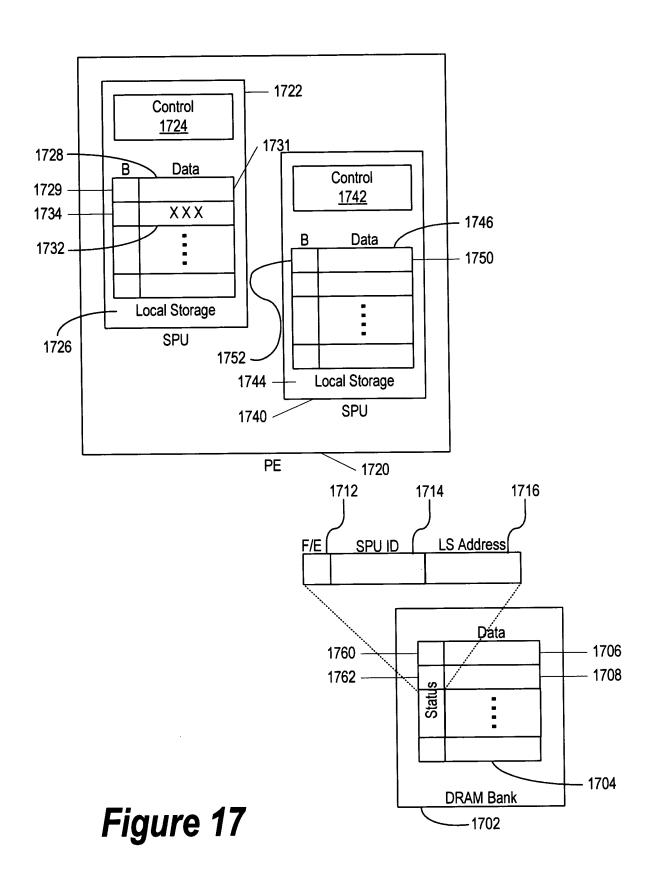


Figure 16

D cket N . AUS920030702US1 Brok nshire, t al.

System and Meth d f r Dynamically Partitioning Pr cessing Acr ss Plurality f Heter gene us Pr cess rs

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System and M thod for Dynamically Partitioning Pr cessing Across Plurality of Heterogen ous Pr cess rs

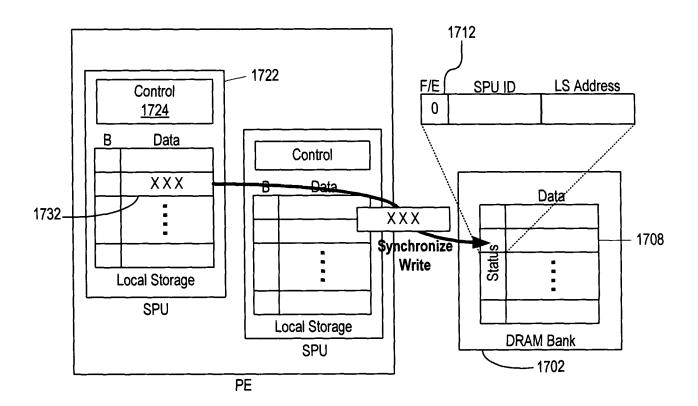


Figure 18

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System and Method f r Dynamically Partiti ning Pr cessing Acr ss Plurality of Heter geneous Pr cess rs

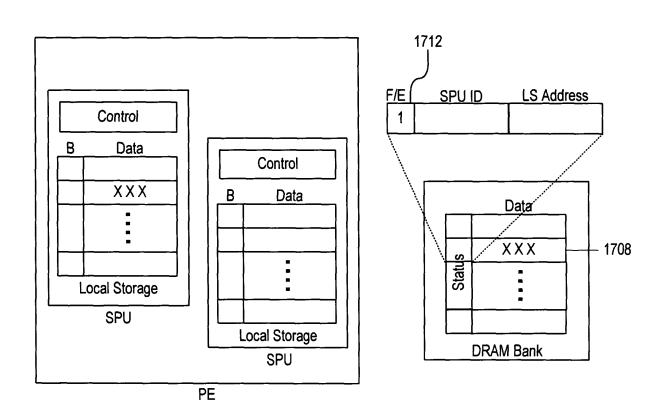
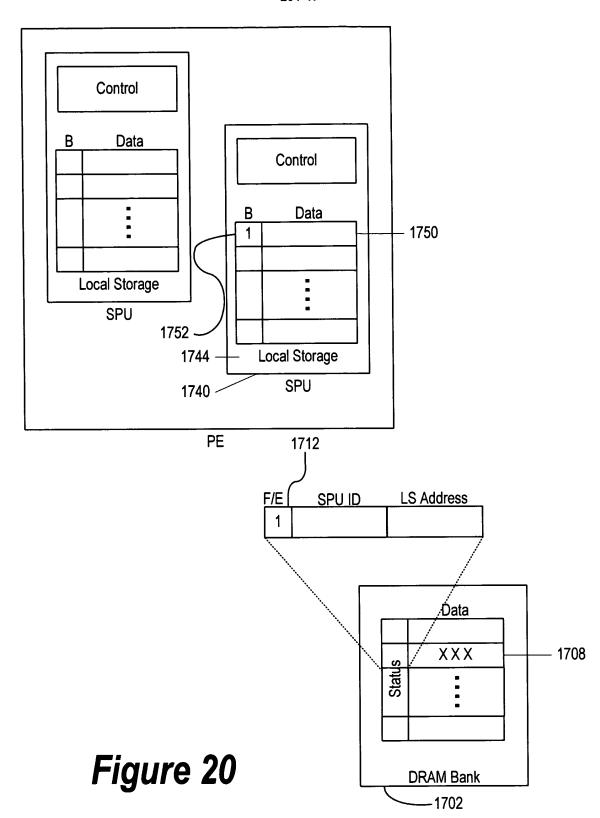


Figure 19

Docket N . AUS920030702US1 Br k nshire, tal.

System and M th df r Dynamically Partiti ning Pr c ssing Across Plurality f Heter gen us Proc ss rs



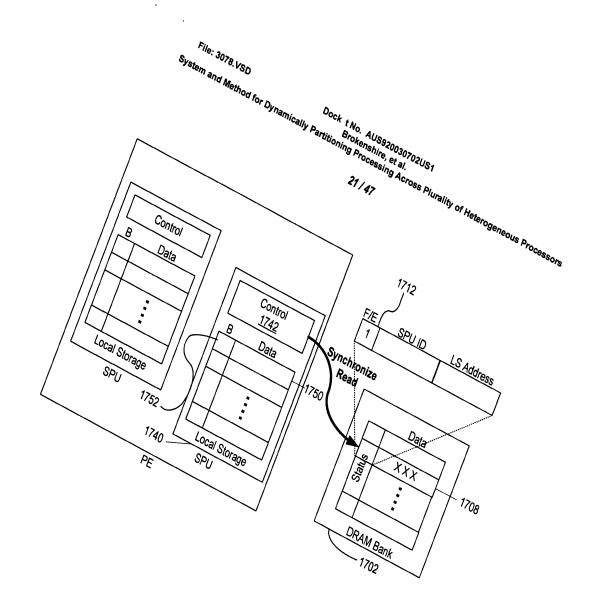
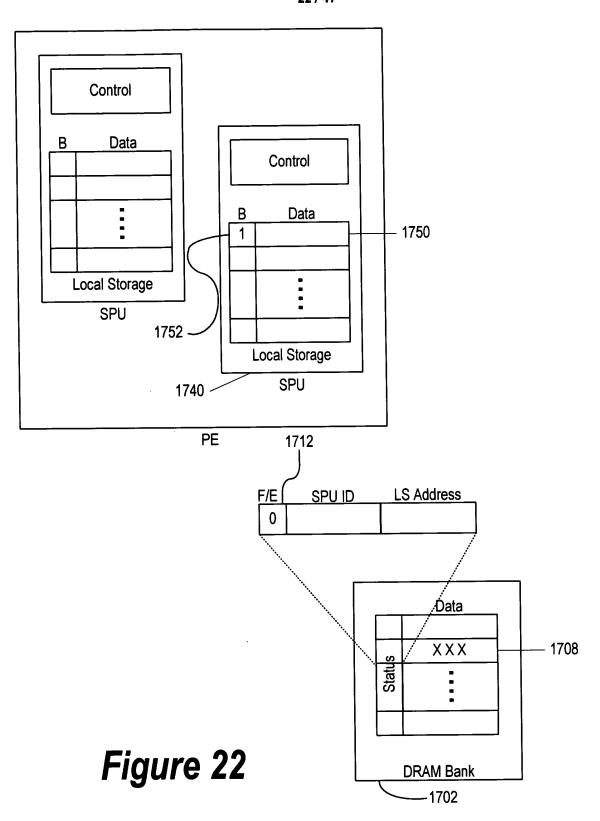


Figure 21

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System and M th d f r Dynamically Partiti ning Pr cessing Acr ss Plurality of Heterogeneous Pr c ssors

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System and M th d f r Dynamically Partiti ning Pr cessing Acr ss Plurality of Heterogen us Pr cess rs

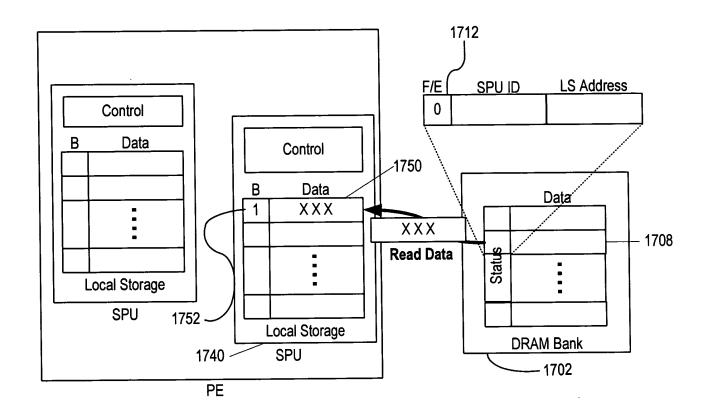
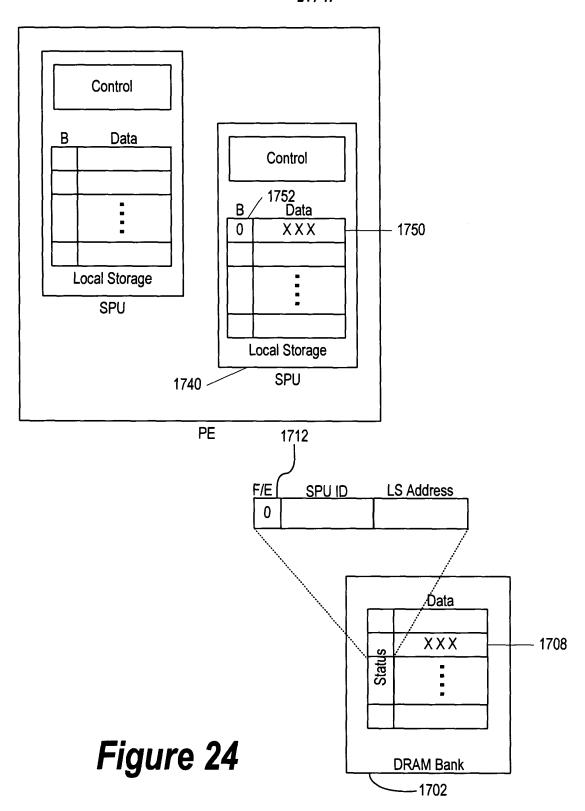


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System and Method for Dynamically Partiti ning Processing Across Plurality of Heter gene us Pr cessors

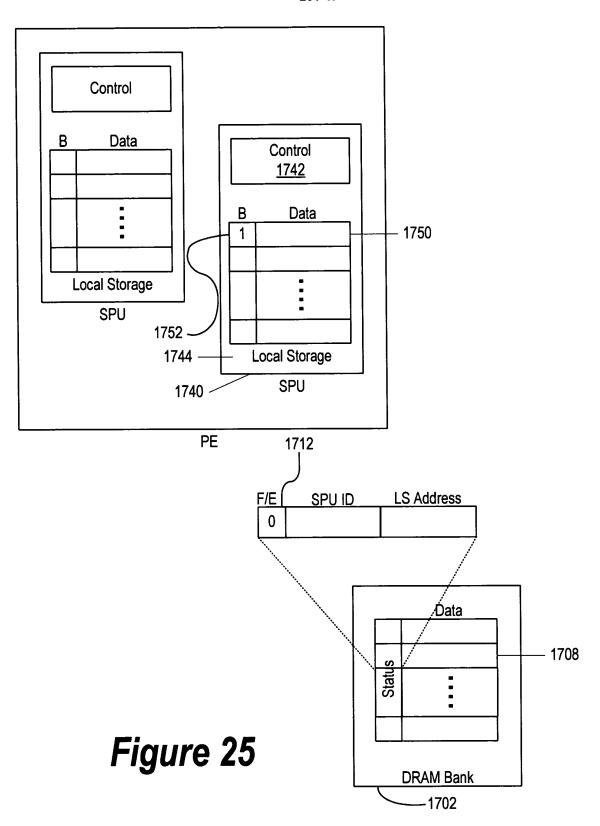
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D ck t No. AUS920030702US1 Br kenshire, et al.

System and Method f r Dynamically Partiti ning Pr cessing Acr ss Plurality f H ter g n ous Proc ssors

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D cket N . AUS920030702US1 Br kenshire, tal.

System and M th d for Dynamically Partiti ning Pr c ssing Across Plurality f H t r g n us Processors

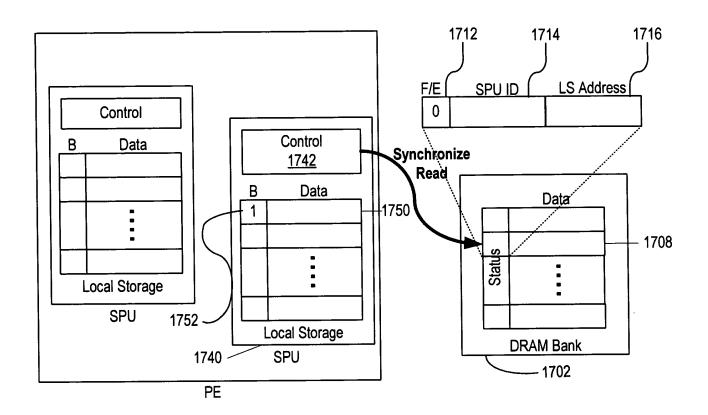


Figure 26

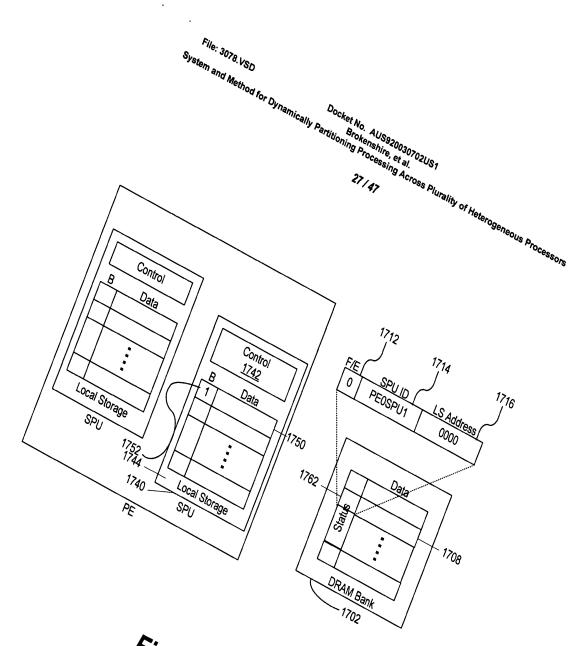


Figure 27

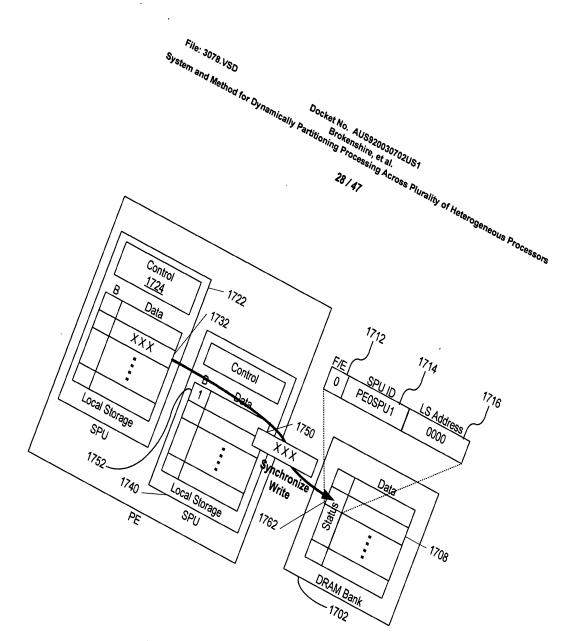


Figure 28

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System and Method for Dynamically Partitioning Pr cessing Across Plurality of Heterogeneous Proc ss rs

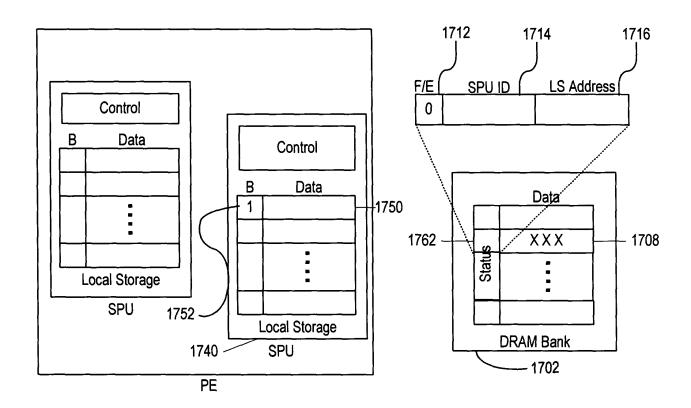


Figure 29

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System and Method f r Dynamically Partiti ning Proc ssing Acr ss Plurality of H ter gene us Proc ssors

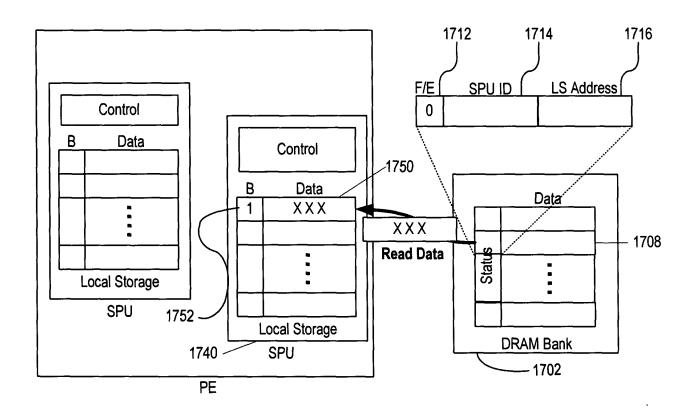


Figure 30

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Brok nshire, t al.

System and M thod for Dynamically Partiti ning Proc ssing Across Plurality of Heterog n ous Proc ssors

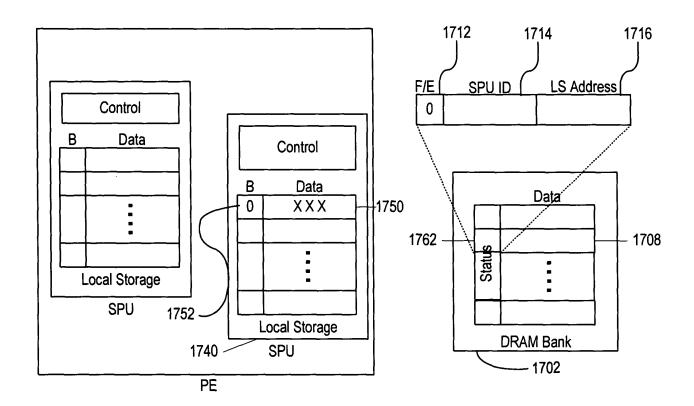


Figure 31

D cket No. AUS920030702US1 Br kenshire, t al.

System and Method for Dynamically Partiti ning Pr cessing Across Plurality f Heterogeneous Pr cessors

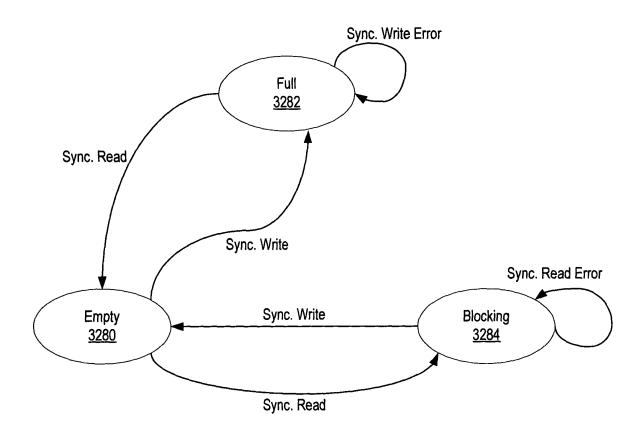


Figure 32

D cket N . AUS920030702US1 Brokenshire, et al.

System and Method for Dynamically Partitioning Proc ssing Across Plurality of Heterogen ous Process rs

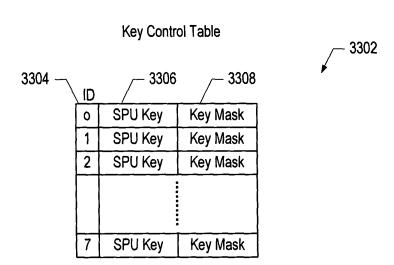


Figure 33

Docket N . AUS920030702US1 Brokenshire, et al.

System and Method f r Dynamically Partiti ning Pr c ssing Across Plurality of Heterogen ous Processors

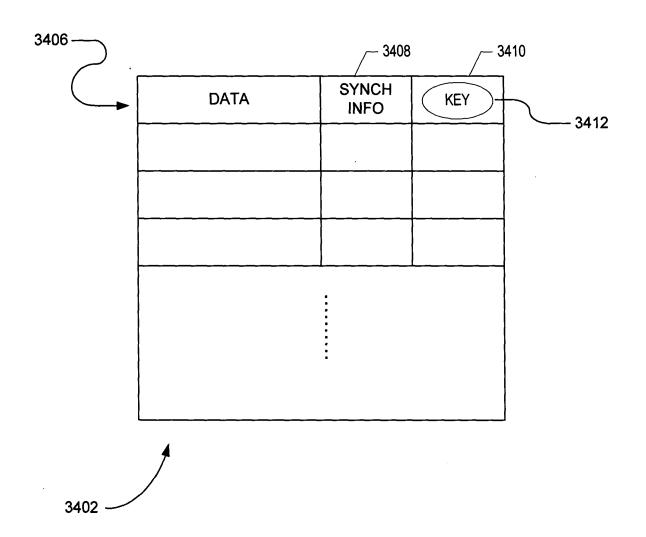


Figure 34

D ck t N . AUS920030702US1

Br kenshire, tal.

System and Method f r Dynamically Partiti ning Pr c ssing Acr ss Plurality of H t r gene us Process rs

Memory Access Control Table

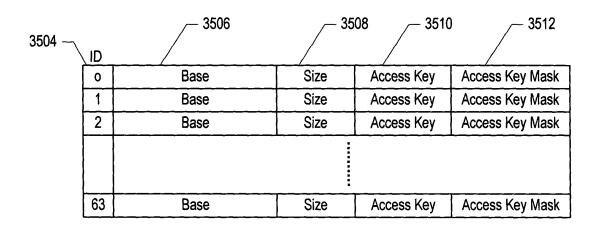




Figure 35

D cket N . AUS920030702US1 Brok nshire, t al.

System and Meth d for Dynamically Partitioning Processing Acr ss Plurality of H terogene us Proc ssors

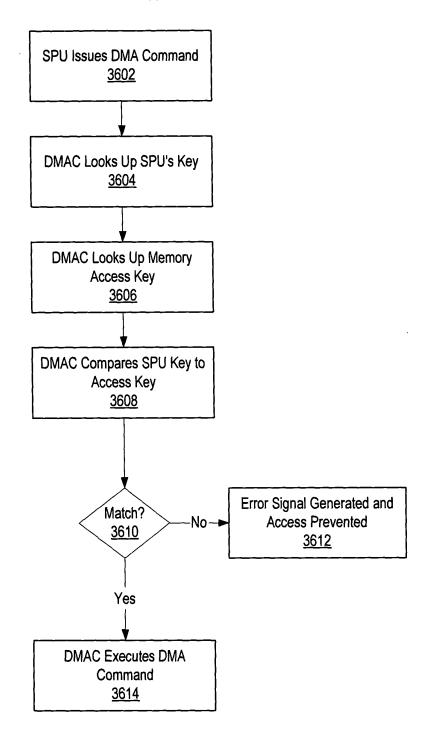


Figure 36

D cket N . AUS920030702US1 Br k nshire, et al.

System and Method for Dynamically Partiti ning Pr cessing Across Plurality f Heter g neous Pr cess rs

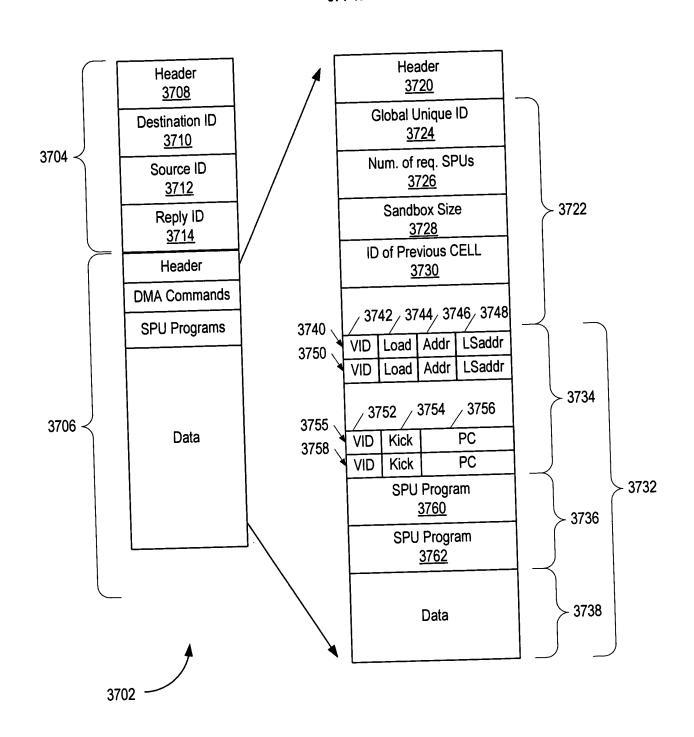


Figure 37

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D cket N . AUS920030702US1 Brokenshire, et al.

System and Meth df r Dynamically Partiti ning Pr c ssing Acr ss Plurality fH ter geneous Process rs

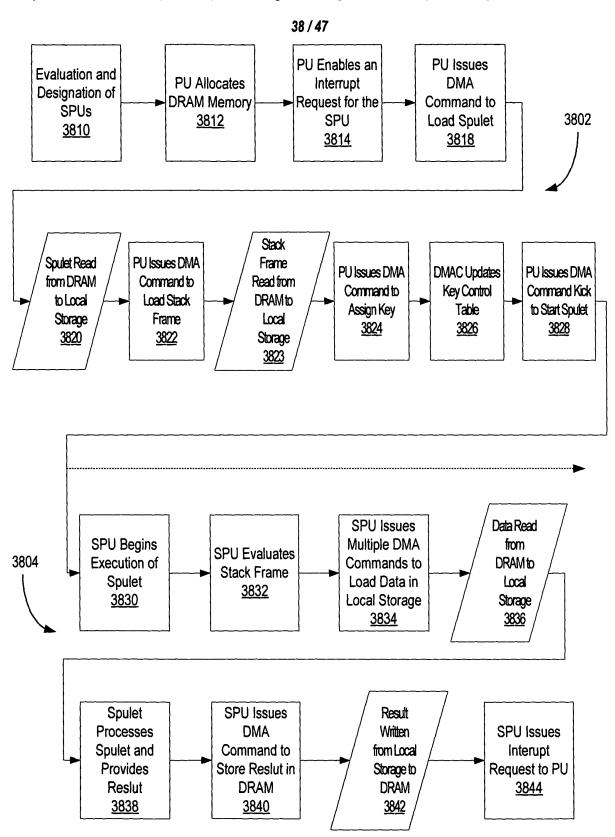
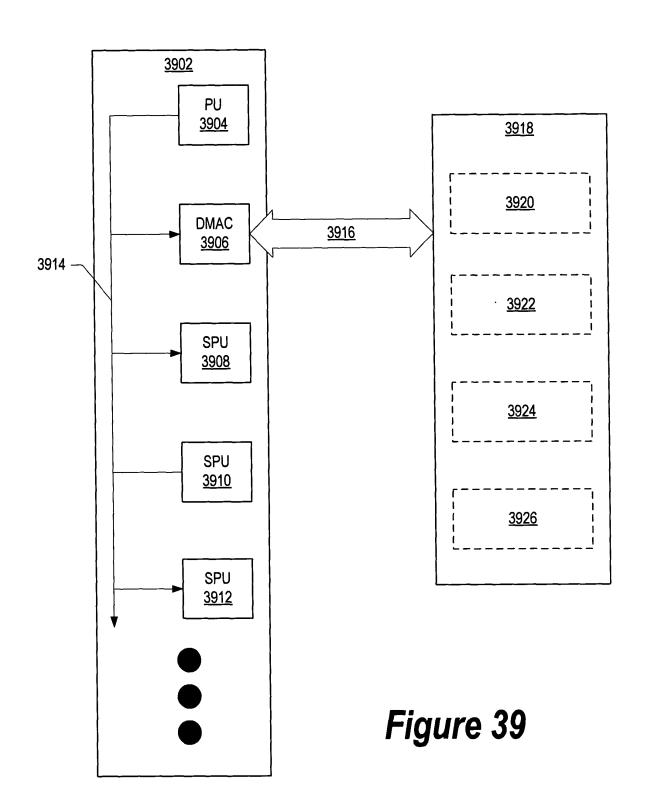


Figure 38

System and Meth df r Dynamically Partiti ning Pr cessing Acr ss Plurality of Heter g n us Pr cessors



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System and M thod for Dynamically Partitioning Proc ssing Across Plurality of Het rogen ous Proc ssors

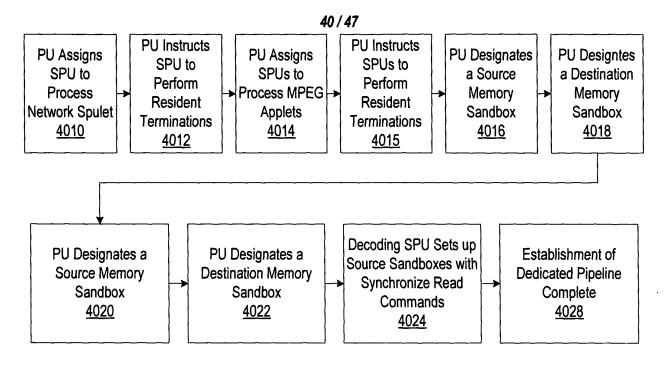


Figure 40A

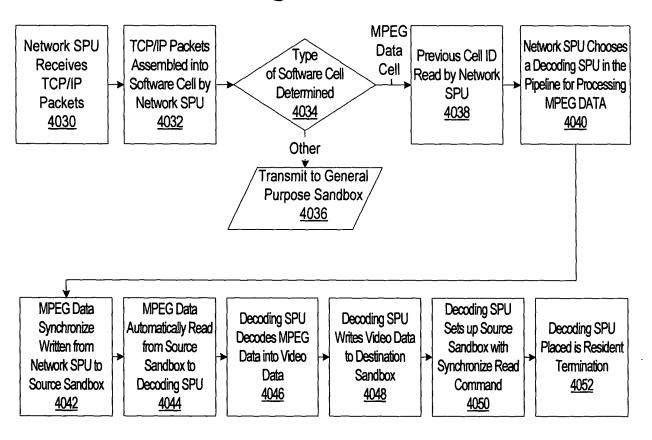


Figure 40B

D cket N . AUS920030702US1 Br kenshire, et al.

System and Meth d for Dynamically Partiti ning Pr cessing Across Plurality of Heter g neous Process rs

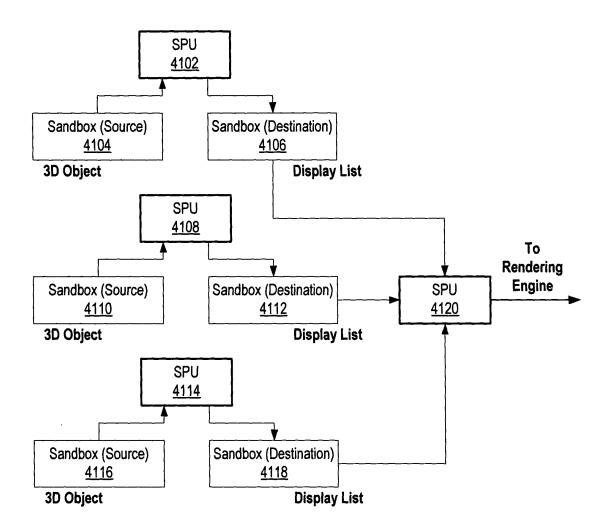


Figure 41

D cket No. AUS920030702US1 Br kenshire, t al.

System and Method for Dynamically Partitioning Pr c ssing Acr ss Plurality of Heterogene us Process rs

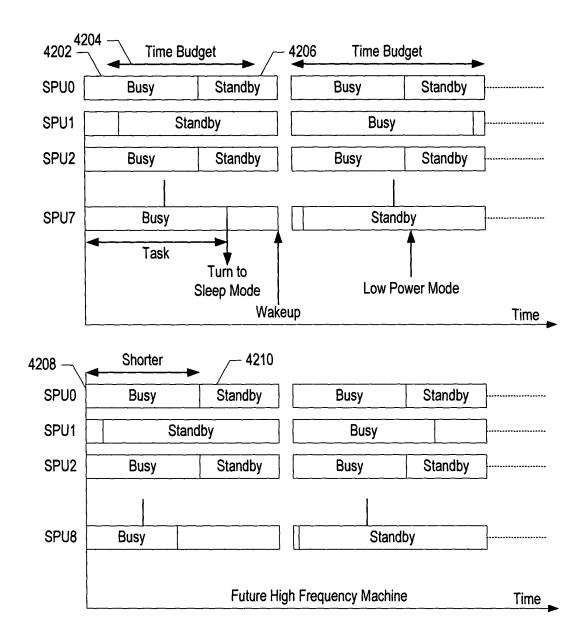
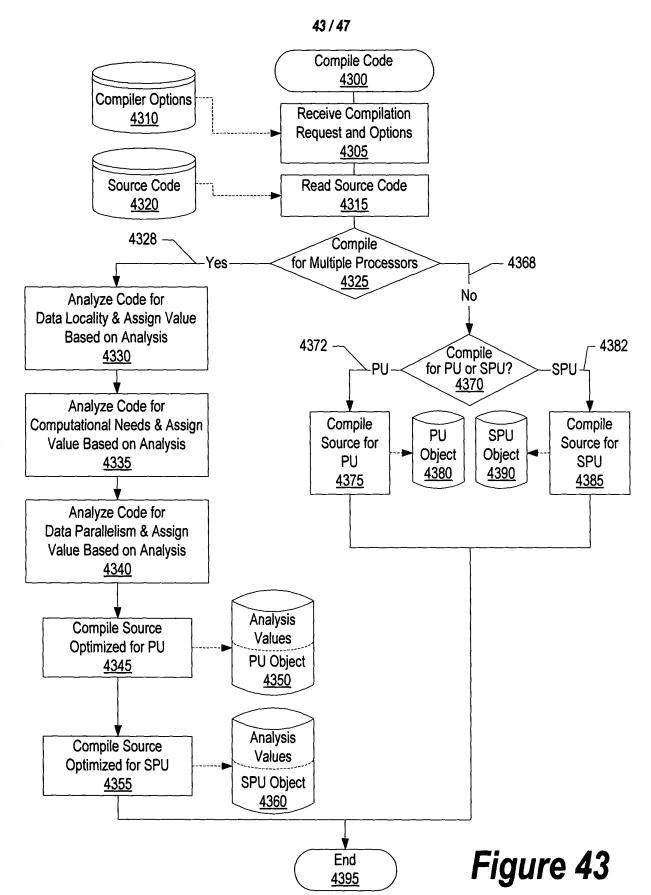


Figure 42

Dock t No. AUS920030702US1 Brok nshire, t al.

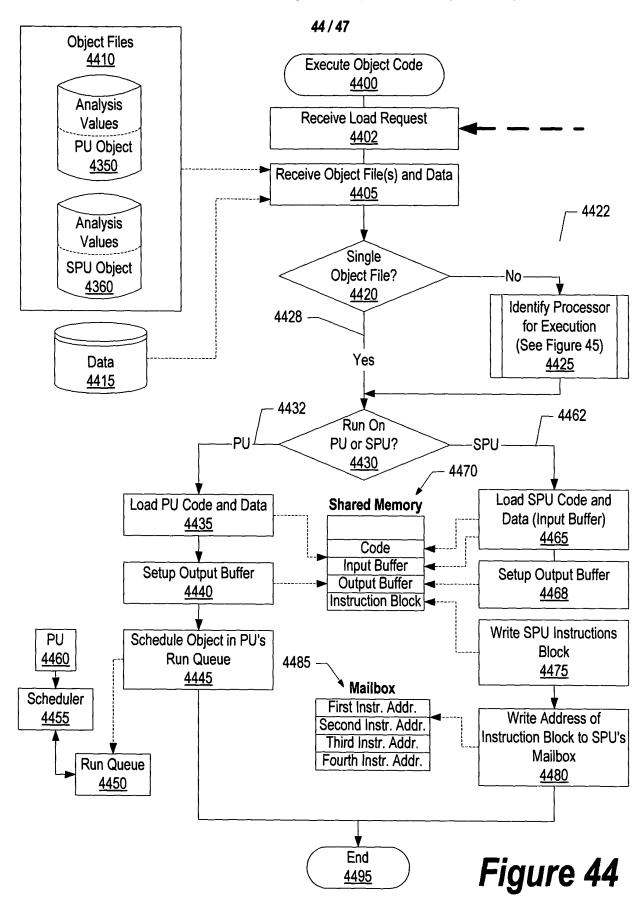
System and Meth d for Dynamically Partitioning Proc ssing Acr ss Plurality of Heterogene us Process rs



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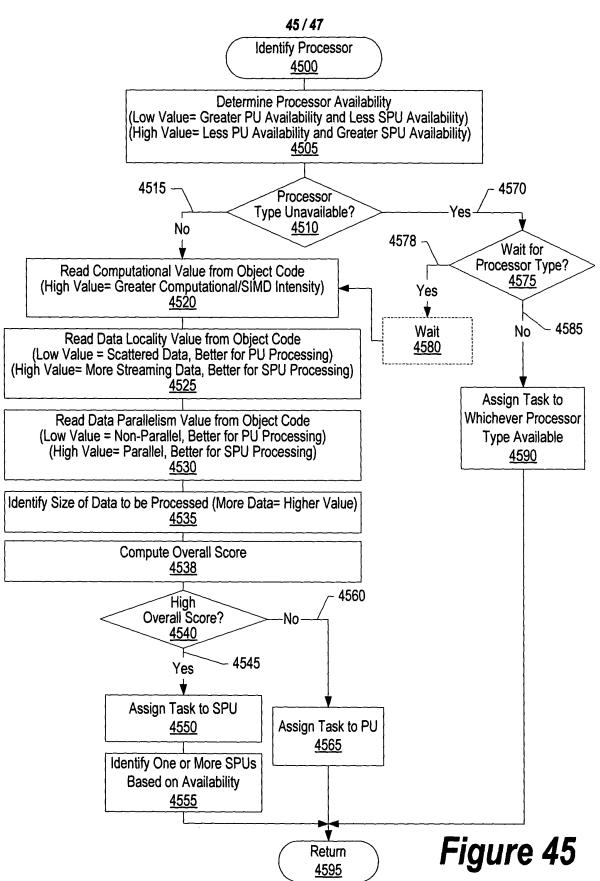
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System and Meth df r Dynamically Partiti ning Pr c ssing Acr ss Plurality of Heterogene us Process rs



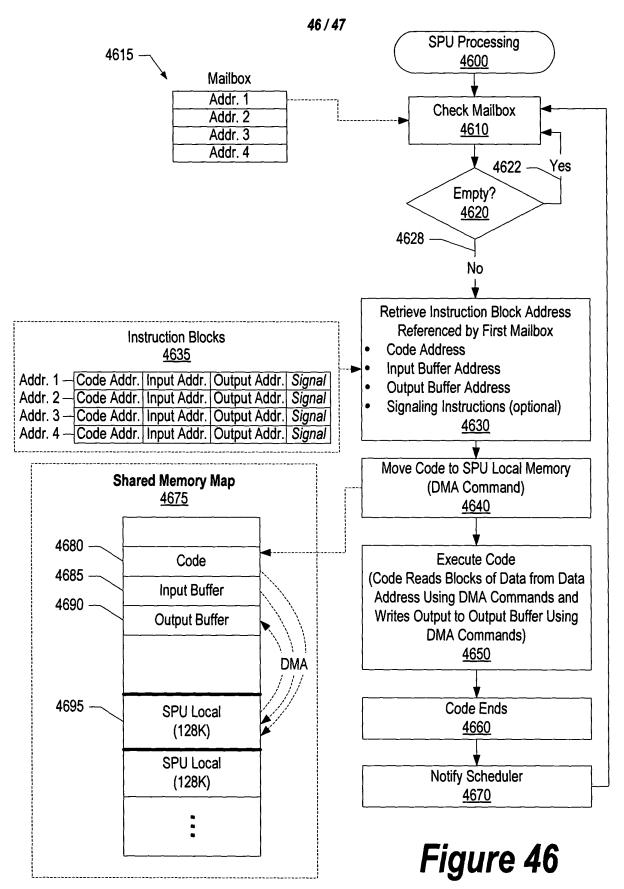
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System and Meth df r Dynamically Partiti ning Proc ssing Acr ss Plurality of Heterogeneous Pr cess rs



Docket N . AUS920030702US1 Br kenshire, et al.

System and Method f r Dynamically Partiti ning Pr c ssing Acr ss Plurality f Heterogeneous Process rs



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System and Method for Dynamically Partitioning Processing Across Plurality of Heterogeneous Processors

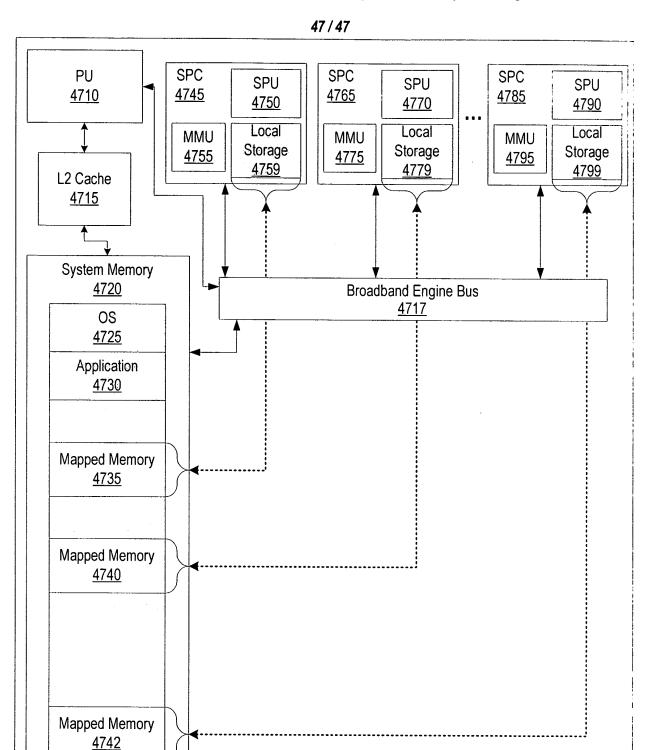


Figure 47

Processor Element 4705